



# HM82C206

## INTEGRATED PERIPHERALS CONTROLLER

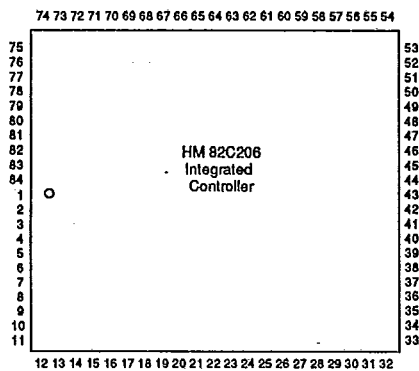
### Features

- Fully compatible to INTEL™'S 8237 DMA controller, 8259 Interrupt controller, 8254 timer/counter, and motorola™'S 146814 real time clock
- Offers 7 DMA channels, 13 Interrupt request channels, 2 timer/counter channels, and a real time clock
- 114 bytes of CMOS RAM memory
- 10MHz DMA clock with programmable internal divider for 5MHz operation
- Programmable wait states for the DMA cycle
- 16 M bytes DMA address space

### Functions

- DMA control logic
- DMA address mapping logic
- Configuration registers
- Interrupt control logic
- Timer/counter logic
- Real time clock logic
- Wait state generation logic

### Pin Assignment



### Pin Number/Name

1	IRQ5		22	GATE2	43	XA0	64	AEN16							
2	IRQ4	12	V <sub>SS</sub>	23	TMRCLK	33	ACK	44	DREQ0	54	XIOR	65	ADSTB16	75	V <sub>CC</sub>
3	IRQ3	13	XA16	24	XD7	34	XA9	45	DREQ1	55	DACK7	66	ADSTB8	76	IRQ15
4	IRQ1	14	PWRGD	25	XD6	35	XA8	46	DREQ2	56	DACK6	67	TC	77	IRQ14
5	A23	15	PSRSTB	26	XD5	36	XA7	47	DREQ3	57	DACK5	68	IOCHRDY	78	IRQ13
6	A22	16	INTA	27	XD4	37	XA6	48	DACK0	58	DREQ7	69	HRQ	79	IRQ12
7	A21	17	TEST	28	XD3	38	XA5	49	DACK1	59	DREQ6	70	INTR	80	IRQ11
8	A20	18	RESET	29	XD2	39	XA4	50	DACK2	60	DREQ5	71	AS	81	IRQ10
9	A19	19	OUT2	30	XD1	40	XA3	51	DACK3	61	DMAMEMR	72	OSCI	82	IRQ9
10	A18	20	OUT1	31	XD0	41	XA2	52	XIOW	62	DMAMEMW	73	HLDA	83	IRQ7
11	A17	21	SCLK	32	V <sub>CC</sub>	42	XA1	53	V <sub>SS</sub>	63	AEN8	74	V <sub>SS</sub>	84	IRQ6