

82C601/A

Single Chip Peripheral Controller

Features

- 100% compatible with IBM PC/ST, AT
- Two 16450 compatible UARTs
- One IBM PC/XT, AT compatible enhanced parallel
- Adapter mode functions
 - Game port decodes
 - Select pin for serial and parallel ports
- Motherboard mode functions
 - IDE interface
 - Real-time-clock chip select
 - General purpose chip select
 - Power saving and power down mode
- 16mA and 24mA output drivers
- Schmitt Trigger RESET input
- Internal address decoders
- EISA ready (motherboard mode)
 - Relocatable ports
 - Relocatable IRQ
- Interrupt sharing capability
- Low power CMOS
- 80-pin pFP or 84-pin PLCC package

The 82C601/A features two 16450 compatible UARTs, an enhanced parallel port, an IDE hard disk interface and chip selects (motherboard mode) or select pins and game port decodes (adapter mode).

Adapter mode where the base addresses are determined by the select pins.

Motherboard mode where all the ports are relocatable, and power management that includes power down for each port, oscillator disable, and chip power down using the PWRGND pin.

The host interface is PC compatible, and can be connected directly to the bus. The system bus interface buffers are capable of sinking 24mA, the parallel port interface signals are capable of sinking 16mA.

Figure 3-3. 82C601/A Block Diagram

