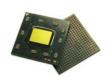


Transmeta[™] Efficeon[™] and Crusoe[™] Processors













| Transmeta Processor Model | Crusoe TM5800 | Crusoe TM5900 | Efficeon TM8600 | Efficeon TM8620 | Efficeon TM8800 |
|---|--|-----------------------------------|-------------------------------------|------------------------------------|----------------------------------|
| Processor Speed | 667 MHz - 1.0 GHz | 800 MHz - 1.0 GHz | 1.0 GHz - 1.1 GHz | 1.0 GHz - 1.1 GHz | 1.4 GHz - 1.6 GHz |
| On-die L1 Instruction Cache | 64 KB | 64 KB | 128 KB | 128 KB | 128 KB |
| On-die L1 Data Cache | 64 KB | 64 KB | 64 KB | 64 KB | 64 KB |
| On-die L2 Write-Back Cache | 512 KB | 512 KB | 1 MB | 1 MB | 1 MB |
| System Bus | PCI | PCI | HyperTransport™ | HyperTransport | HyperTransport |
| System Bus Speed | 33 MHz | 33 MHz | 400 MHz | 400 MHz | 400 MHz |
| Aggregate I/O Total Bandwidth | 266 MB/sec | 266 MB/sec | 1.6 GB/sec | 1.6 GB/sec | 1.6 GB/s |
| Multimedia Instruction Support | MMX | MMX | MMX, SSE-SSE2 | MMX, SSE-SSE2 | MMX, SSE-SSE2 |
| VLIW Processor Engine | 128-bit | 128-bit | 256-bit | 256-bit | 256-bit |
| Instructions Issued Per Clock Cycle | Up to 4 | Up to 4 | Up to 8 | Up to 8 | Up to 8 |
| On-die Integrated Northbridge Functionality | Yes | Yes | Yes | Yes | Yes |
| Integrated memory controller | SDR, DDR 266 | DDR 266 | DDR 266, 333 | DDR 266, 333 | DDR 226, 333 |
| Support for ECC memory | No | No | Yes | Yes | Yes |
| Integrated AGP 1X, 2X & 4X graphics interface | No | No | Yes | Yes | Yes |
| Full x86 Software and OS Compatibility | Yes | Yes | Yes | Yes | Yes |
| LongRun [™] Thermal & Power Management | Yes | Yes | Yes - Enhanced | Yes - Enhanced | Yes - Enhanced |
| AntiVirusNX [™] Technology | No | No | No | No | Yes |
| Process Geometry | TSMC 130nm | TSMC 130nm | TSMC 130nm | TSMC 130nm | Fujitsu 90nm |
| Package Size | 32.5mm x 25mm | 21mm x 21mm | 29mm x 29mm | 21mm x 21mm | 29mm x 29mm |
| Junction Temperature (Tj) | Up to 100° C | Up to 100° C | Up to 100° C | Up to 100° C | Up to 100° C |
| Package Type | 474-pin Ceramic BGA 1.27mm ball pitch | 399-pin FC-OBGA 1mm ball pitch | 783-pin OBGA with 1mm ball pitch | 592-pin OBGA with 0.8mm ball pitch | 783-pin OBGA with 1mm ball pitch |

Corporate Overview

Reshaping the Industry through Efficient Computing

Located in Santa Clara, California, Transmeta Corporation designs, develops and sells highly energy efficient microprocessors that deliver a compelling balance of long battery life and high performance. Transmeta processors are designed to maximize computing performance in environments where energy efficiency is important. This in turn allows manufacturers to design new and innovative devices that were not previously possible; devices including highly-mobile handheld ultra-personal computers (UPCs), ultra-portable notebooks that are nearly half an inch thick and dense blade servers that offer more performance by incorporating more processors into tighter spaces.

Thin Client



Optimized for energy efficiency, Transmeta Efficeon™ and Crusoe™ processors include a number of advanced features. The highly integrated nature and small footprint of Crusoe and Efficeon processors facilitates compact, thin and very light computer designs. Transmeta's LongRun[™] dynamic power management technology adjusts processor operating frequency and voltage based upon application performance workloads, reducing heat for guiet, fanless operation and further extends battery life. A unique and innovative approach to design allows Transmeta to incorporate

the latest technologies such as AntiVirusNX™, a cutting-edge security technology that detects and prevents attempts by attackers to spread malicious virus content. These are just a few examples of how Transmeta's leadership in efficient computing continues to drive product and technology innovation.





Tablet PC









Digital Home







Embedded







For more information, visit www.transmeta.com

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