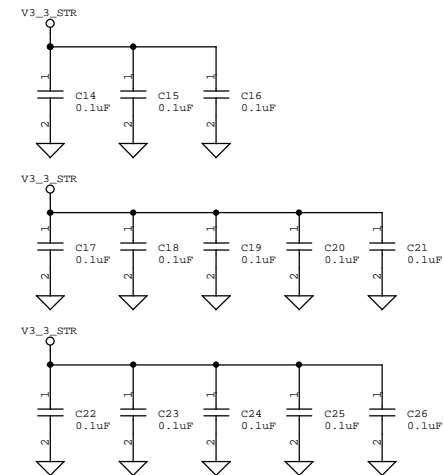
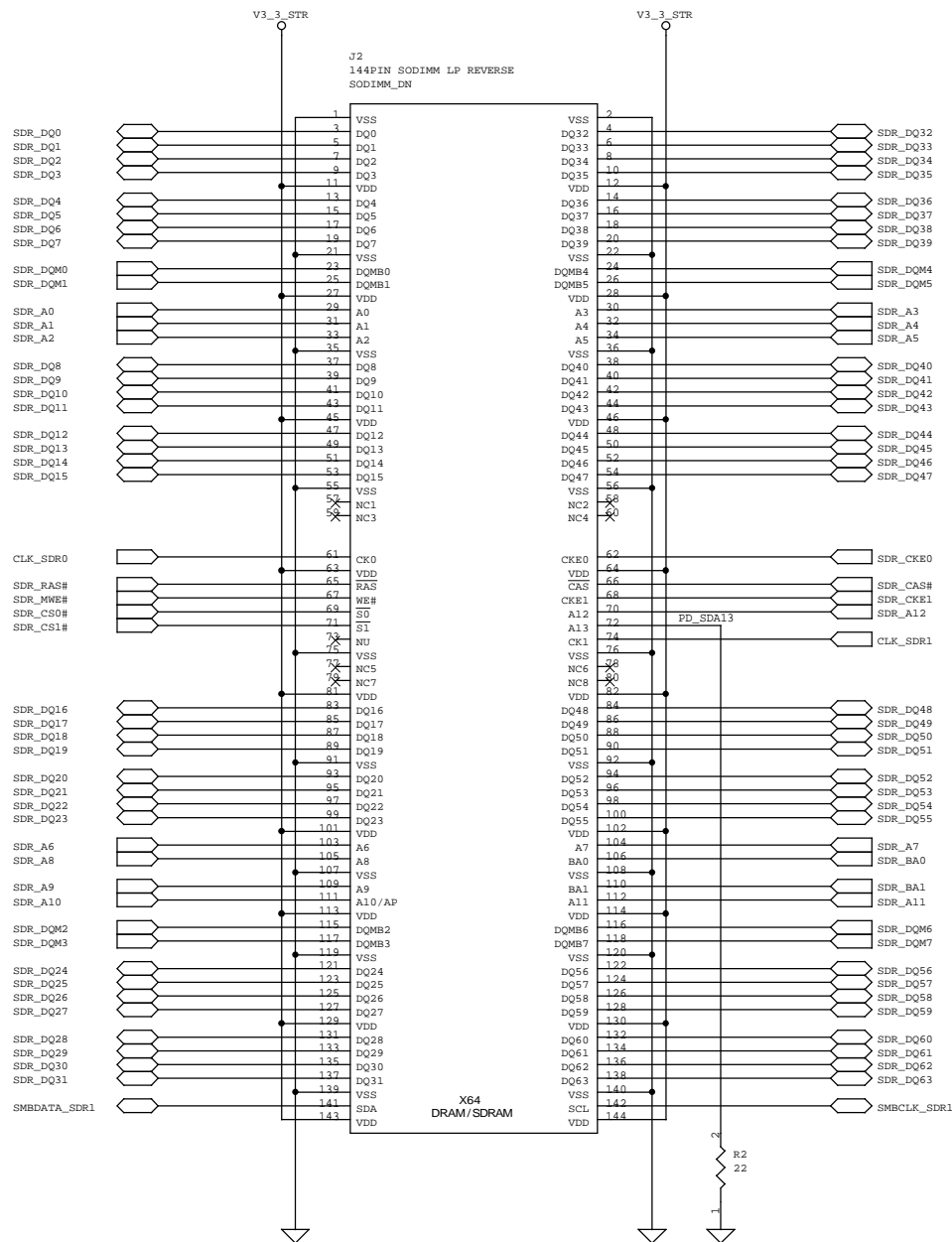


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Project: TM5800 Design Guide			
Title: Dual SDR SODIMM (1/2)			
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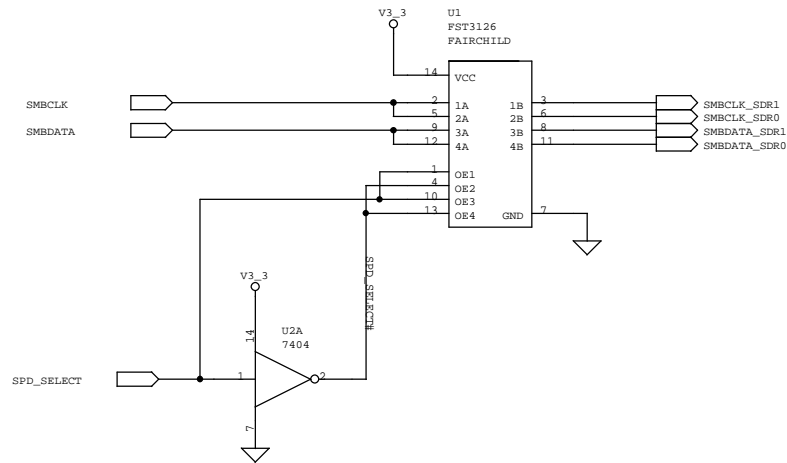


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DUAL SDR SODIMM CONFIGURATIONS:

THE SYSTEM NEEDS THE ABILITY TO ISOLATE
THE SMBCLK AND SMBDATA TO EACH SDR SODIMM

IN THIS CIRCUIT A 1 BIT BUS SWITCH IS USED IN
CONJUNCTION WITH AN INVERTER AND A GPIO



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