

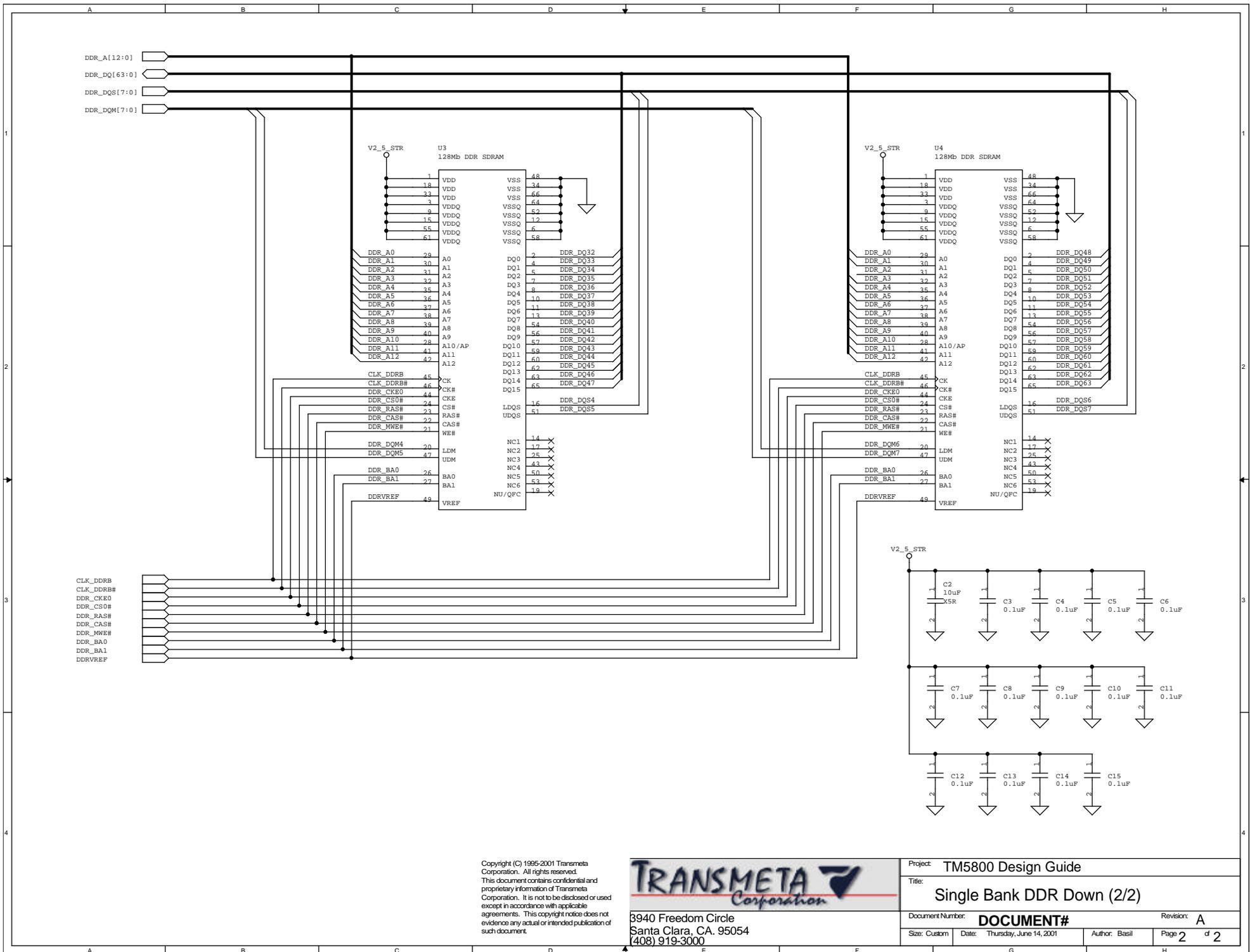
CLK\_DDRA  
 CLK\_DDRA#  
 DDR\_CKE0  
 DDR\_CKE#  
 DDR\_CSO#  
 DDR\_CSO#  
 DDR\_RAS#  
 DDR\_RAS#  
 DDR\_CAS#  
 DDR\_CAS#  
 DDR\_MWE#  
 DDR\_MWE#  
 DDR\_BA0  
 DDR\_BA1  
 DDRVREF

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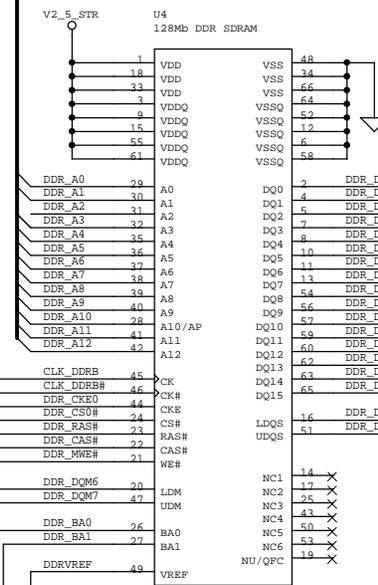
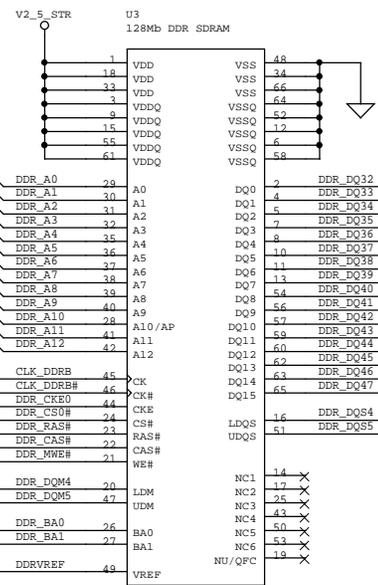
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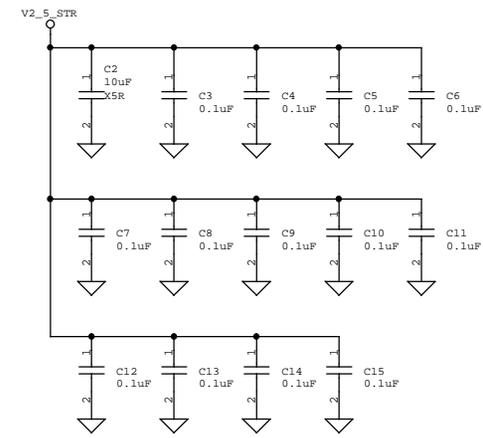
|  |                                 |                                   |             |
|--|---------------------------------|-----------------------------------|-------------|
| Project: <b>TM5800 Design Guide</b>      |                                 | Revision: <b>A</b>                |             |
| Title: <b>Single Bank DDR Down (1/2)</b> |                                 | Document Number: <b>DOCUMENT#</b> |             |
| Size: Custom                             | Date: Friday, November 30, 2001 | Author: Basil                     | Page 1 of 2 |



DDR\_A[12:0]  
 DDR\_DQ[63:0]  
 DDR\_DQS[7:0]  
 DDR\_DQM[7:0]



CLK\_DDRB  
 CLK\_DDRB#  
 DDR\_CKE0  
 DDR\_CS0#  
 DDR\_RAS#  
 DDR\_CAS#  
 DDR\_MWE#  
 DDR\_BA0  
 DDR\_BA1  
 DDVRVREF



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|                  |                               |               |             |
|------------------|-------------------------------|---------------|-------------|
| Project:         | TM5800 Design Guide           |               |             |
| Title:           | Single Bank DDR Down (2/2)    |               |             |
| Document Number: | DOCUMENT#                     | Revision:     | A           |
| Size: Custom     | Date: Thursday, June 14, 2001 | Author: Basil | Page 2 of 2 |