



THE SDRAM CLOCKS ARE NOT NEEDED AS THEY ARE PROVIDED BY THE PROCESSOR

THE PCI CLOCKS ARE SYSTEM IMPLEMENTATION SPECIFIC

SEE THE DESIGN GUIDE FOR INFORMATION REGARDING THE USE OF THE FREE RUNNING PCI CLOCK FOR THE PROCESSOR AND SOUTHBRIDGE

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**TRANSMETA**  
Corporation

3940 Freedom Circle  
Santa Clara, CA. 95054  
(408) 919-3000

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