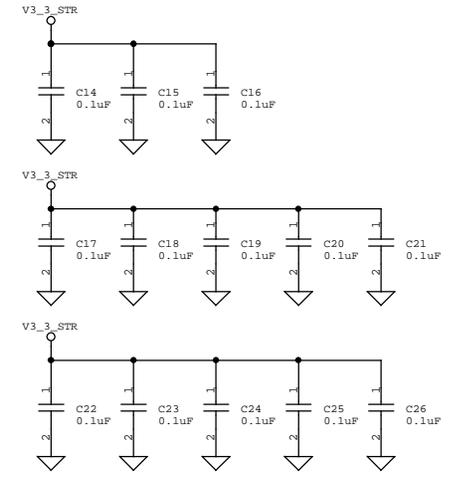
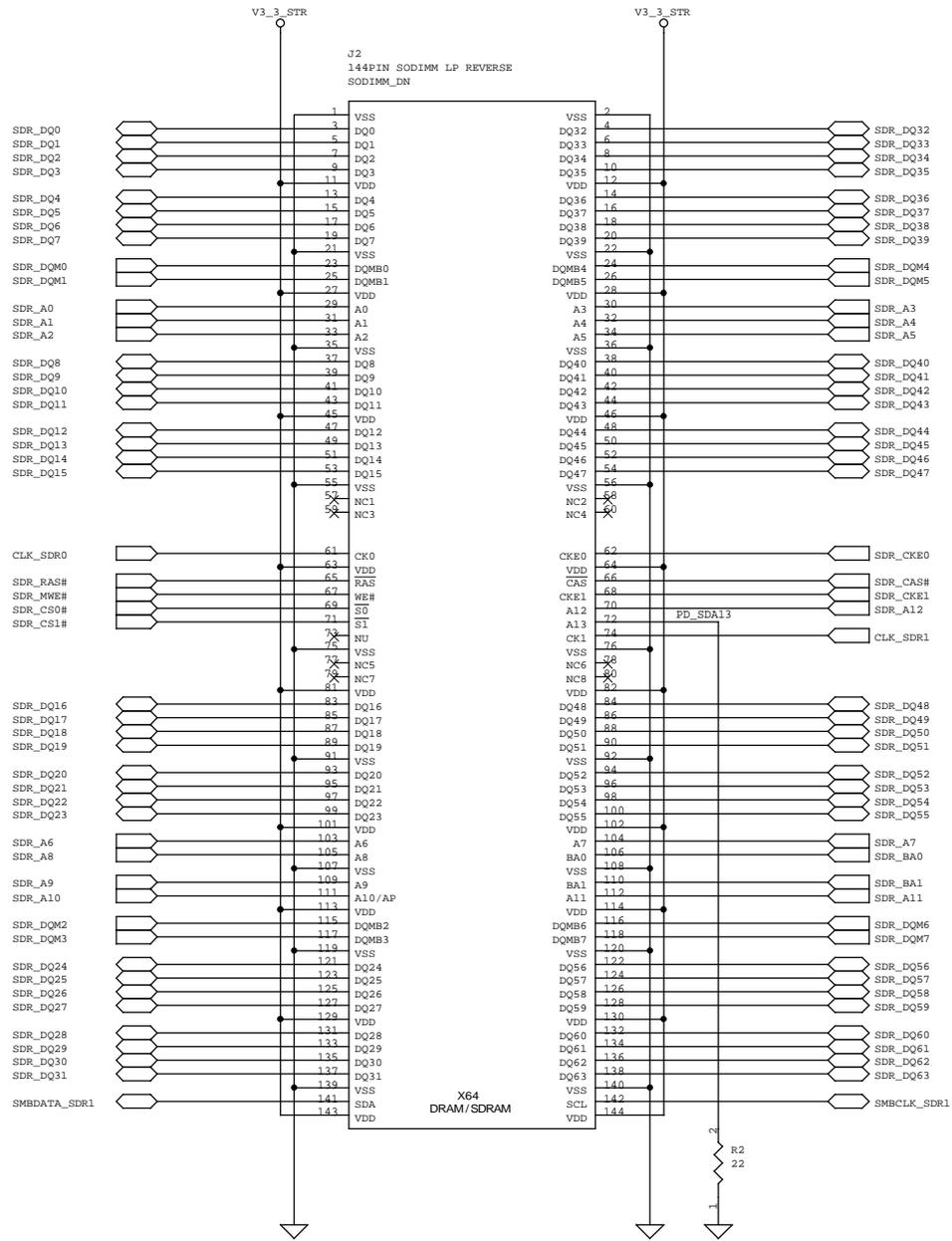


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Corporation

3940 Freedom Circle
Santa Clara, CA. 95054
(408) 919-3000

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Title: Dual SDR SODIMM (1/2)	
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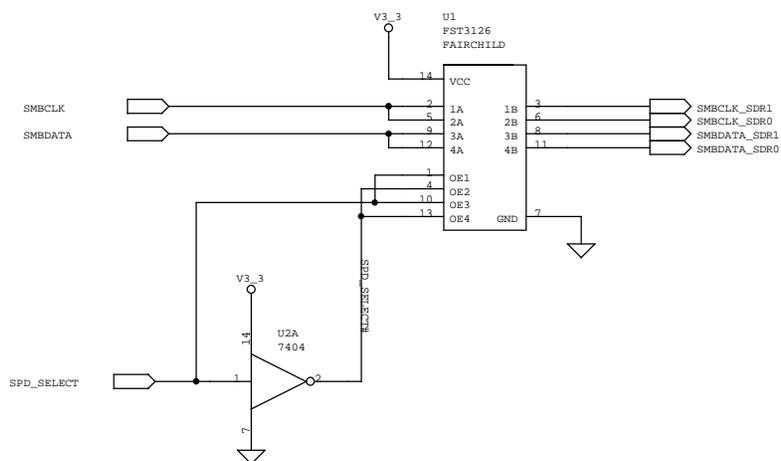
3940 Freedom Circle
Santa Clara, CA. 95054
(408) 919-3000

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DUAL SDR SODIMM CONFIGURATIONS:

THE SYSTEM NEEDS THE ABILITY TO ISOLATE
THE SMBCLK AND SMBDATA TO EACH SDR SODIMM

IN THIS CIRCUIT A 1 BIT BUS SWITCH IS USED IN
CONJUNCTION WITH AN INVERTER AND A GPIO



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