



# 8XC251Tx (8XC251TA, TB, TP, TQ) SPECIFICATION UPDATE

Release Date: December, 1998

Order Number: 273221-001

**Notice:** The 8XC251Tx may contain design defects or errors known as errata. Characterized errata that may cause the 8XC251Tx's behavior to deviate from published specifications are documented in this specification update.

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Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

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## REVISION HISTORY

Rev. Date	Version	Description
12/03/98	001	<i>This is the new Specification Update document. It contains all identified errata published prior to this date.</i>

## PREFACE

As of July, 1996, Intel's Computing Enhancement Group has consolidated available historical device and documentation errata into this new document type called the Specification Update. We have endeavored to include all documented errata in the consolidation process, however, we make no representations or warranties concerning the completeness of the Specification Update.

This document is an update to the specifications contained in the Affected Documents/Related Documents table below. This document is a compilation of device and documentation errata, specification clarifications and changes. It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools.

Information types defined in Nomenclature are consolidated into the specification update and are no longer published in other documents.

This document may also contain information that was not previously published.

### ***Affected Documents/Related Documents***

Title	Order
8XC251TA/TB/TP/TQ High Performance CHMOS Microcontroller datasheet - Commercial/Express	273129

## ***Nomenclature***

**Errata** are design defects or errors. These may cause the published (component, board, system) behavior to deviate from published specifications. Hardware and software designed to be used with any component, board, and system must consider all errata documented.

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in any new release of the specification.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in any new release of the specification.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These will be incorporated in any new release of the specification.

**NOTE:Errata remain in the specification update throughout the product's lifecycle, or until a particular stepping is no longer commercially available. Under these circumstances, errata removed from the specification update are archived and available upon request. Specification changes, specification clarifications and documentation changes are removed from the specification update when the appropriate changes are made to the appropriate product specification or user documentation (datasheets, manuals, etc.).**

## SUMMARY TABLE OF CHANGES

The following table indicates the errata, specification changes, specification clarifications, or documentation changes which apply to the 8XC251 TA/TB/TP/TQ product. Intel may fix some of the errata in a future stepping of the component, and account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### *Codes Used in Summary Table*

#### ***Stepping***

X: Errata exists in the stepping indicated. Specification Change or Clarification that applies to this stepping.

(No mark)

or (Blank box): This erratum is fixed in listed stepping or specification change does not apply to listed stepping.

#### ***Page***

(Page): Page location of item in this document.

#### ***Status***

Doc: Document change or update will be implemented.

Fix: This erratum is intended to be fixed in a future step of the component.

Fixed: This erratum has been previously fixed.

NoFix: There are no plans to fix this erratum.

Eval: Plans to fix this erratum are under evaluation.

#### ***Row***

**|** Change bar to left of table row indicates this erratum is either new or modified from the previous version of the document.



**Errata**

REV. Date	Steppings			Page	Status	ERRATA
	#	#	#			
						<i>None for this revision of this specification update.</i>

**Specification Changes**

Rev. #	Steppings			Page	Status	SPECIFICATION CHANGES
	#	#	#			
						<i>None for this revision of this specification update.</i>

**Specification Clarifications**

Rev. #	Steppings			Page	Status	SPECIFICATION CLARIFICATIONS
	#	#	#			
						<i>None for this revision of this specification update.</i>

**Documentation Changes**

Rev. #	Document Revision	Page	Status	DOCUMENTATION CHANGES
1	273129-001	10	Doc	<i>1. Page 28, Table 14</i>

## IDENTIFICATION INFORMATION

### *Markings*

None for this revision of this specification update.

## ERRATA

None for this revision of this specification update.

## SPECIFICATION CHANGES

None for this revision of this specification update.

## SPECIFICATION CLARIFICATIONS

None for this revision of this specification update.

## DOCUMENTATION CHANGES

### 1. *Page 28, Table 14*

**PROBLEM:** The value for Output Data Hold After Clock Rising Edge (TXHQX) is wrongly stated as  $2T_{osc}-117$  ns. The correct TXHQX value should be  $2T_{osc}-50$  ns.

**AFFECTED PRODUCTS:** *8XC251TA/TB/TP/TQ High Performance CHMOS Microcontroller* datasheet - Commercial/Express.