



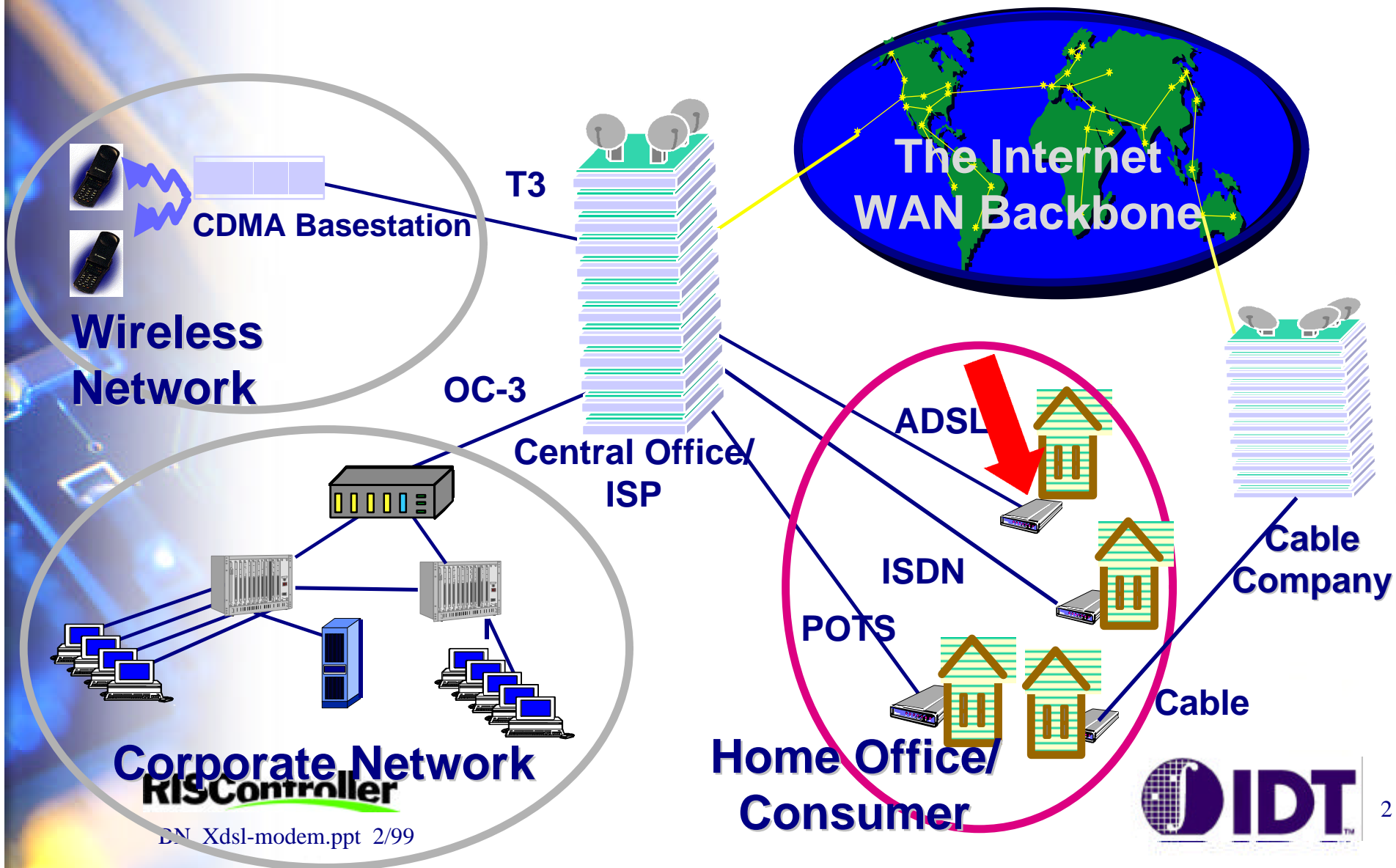
# ADSL Modems Example using RC32364 CPU and RC32134 Support Chip

**RISController**

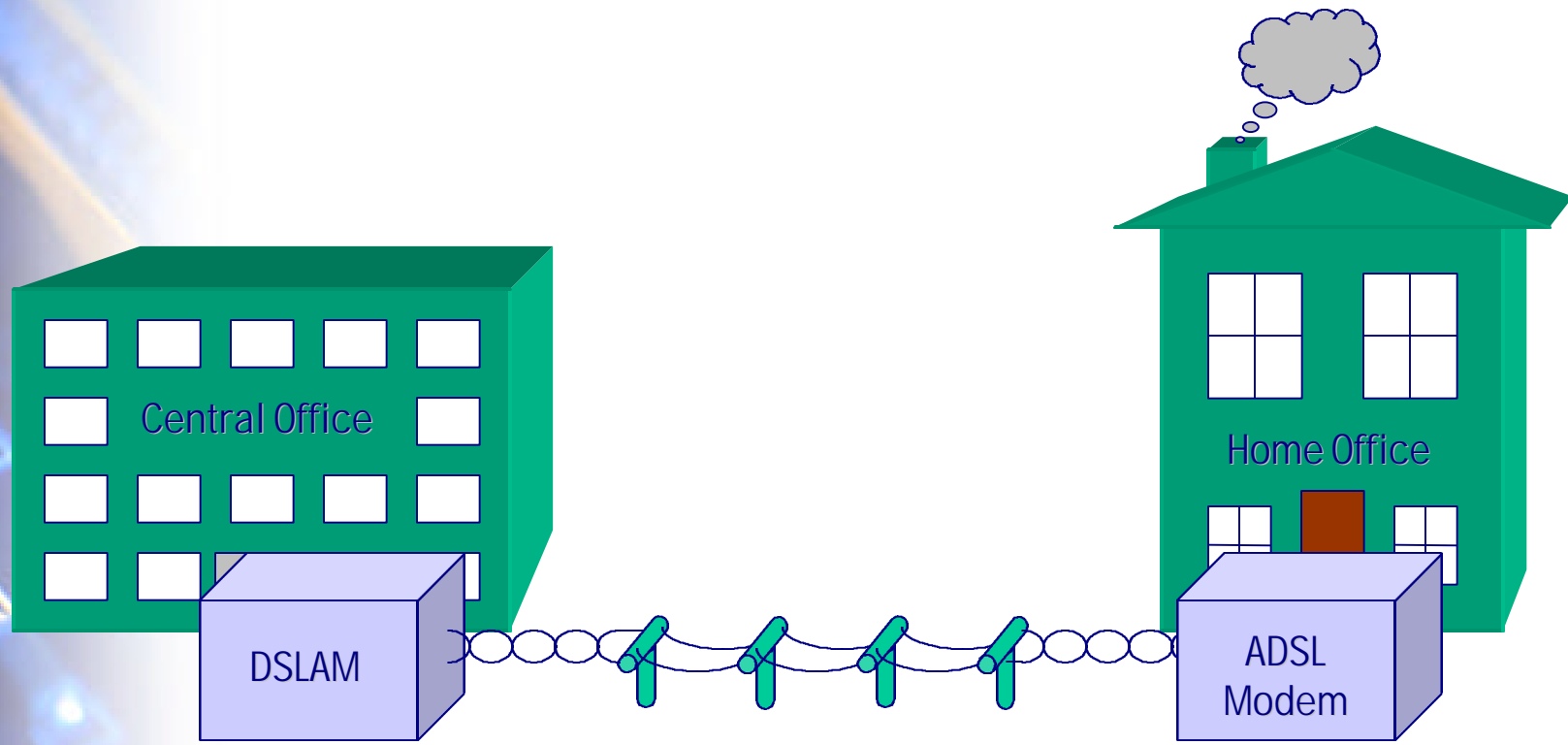
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# xDSL Modems



# Matching CPU to function



RC4650

RC64475

RC5000

Code Compatible

RC32364

**RISController**

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# ADSL Modem Requirements



- q Rate adaptable from 1.5 Mbps to 8 Mbps over existing twisted pair for the home or small office
- q CPU horse power for ...
  - ò DMT algorithms plus overhead for signaling, protocol conversion, etc.
  - ò Routing Ethernet and possibly USB
  - ò SoftSAR
- q Bottom line: consumer cost less than \$250
- q RISController CPUs
  - ò RC32364
    - à RC32134 System controller

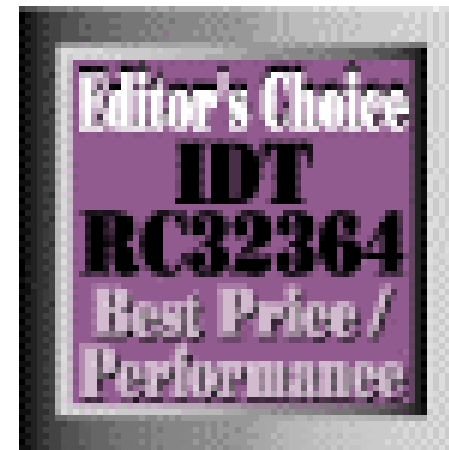
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# Award Winning ...



Microprocessor Report, January 25 1999

**RISController**

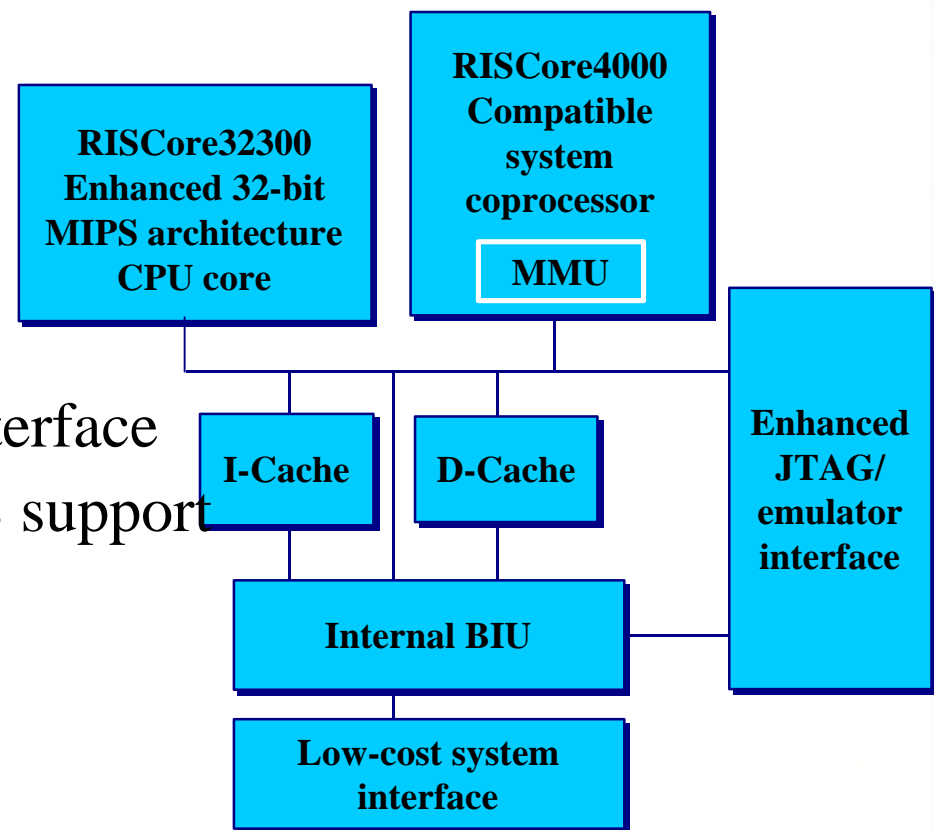
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# RC32364: Advanced Generation 32-bit Architecture



- q 100/133MHz (175 dhrystones)
- q 8k I / 2k D caches, lockable per line
- q 32-bit enhanced architecture
  - ò Non-blocking loads
  - ò Cache pre-fetch support
  - ò Enhanced DSP capability
- q Programmable CPU/bus clock
- q 8/16/32-bit configurable Bus interface
- q Windows CE compatible/RTOS support
- q Static 3.3V core, low-power (.8W @ 100MHz)
- q On-chip debug interface
- q Industrial Temp.

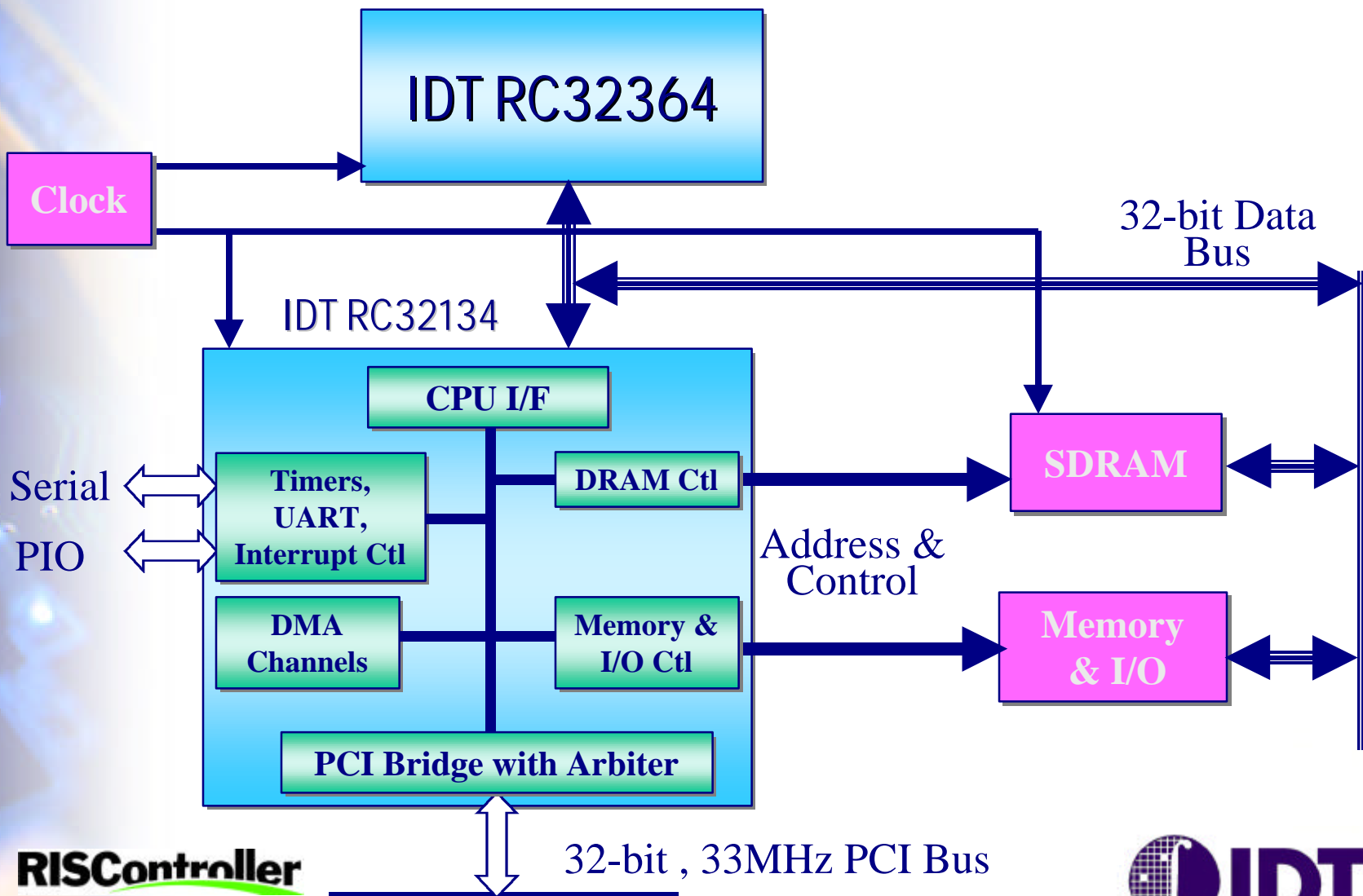


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# Typical System using the RC32364 ...



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# RC32134 Features



- q Direct CPU interface
  - ò up to 75 MHz maximum
- q Direct DRAM control (SDRAM / EDO)
  - ò SyncDRAM:
    - â 4 banks, 2 to 16-M devices
  - ò EDO
    - â 4 banks, 4 to 32-M devices
- q Local memory, I/O interface
  - ò Supports RAM, Flash/ROM, Dual-Ports and peripherals
  - ò 6-chip selects
    - â 8-, 16- and 32-bit wide
    - â Variable latency
  - ò Supports 8-bit boot PROM
- q 32-bit, 33-MHz PCI bridge
  - ò Asynchronous to CPU clock
  - ò Endian-ness byte swapping
  - ò Host or satellite capability with built-in arbiter
  - ò Plug-and-play support
- q Scatter/gather 4-channel DMA controller
- q Dual channel 16552 compatible UART
- q Serial Peripheral Interface
- q Parallel I/O
- q Timer/counters

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# xDSL Modem Solution Elements



- q RC32364 RISController CPU
  - ò Soft SAR
  - ò DMT software drivers
- q RC32134 System Controller
  - ò PCI Interface,
  - ò DMA Controller
  - ò Generic memory and IO control
- q Metalink (or equiv Analog Front End)
- q ADSL transceiver from MetaLink or equiv

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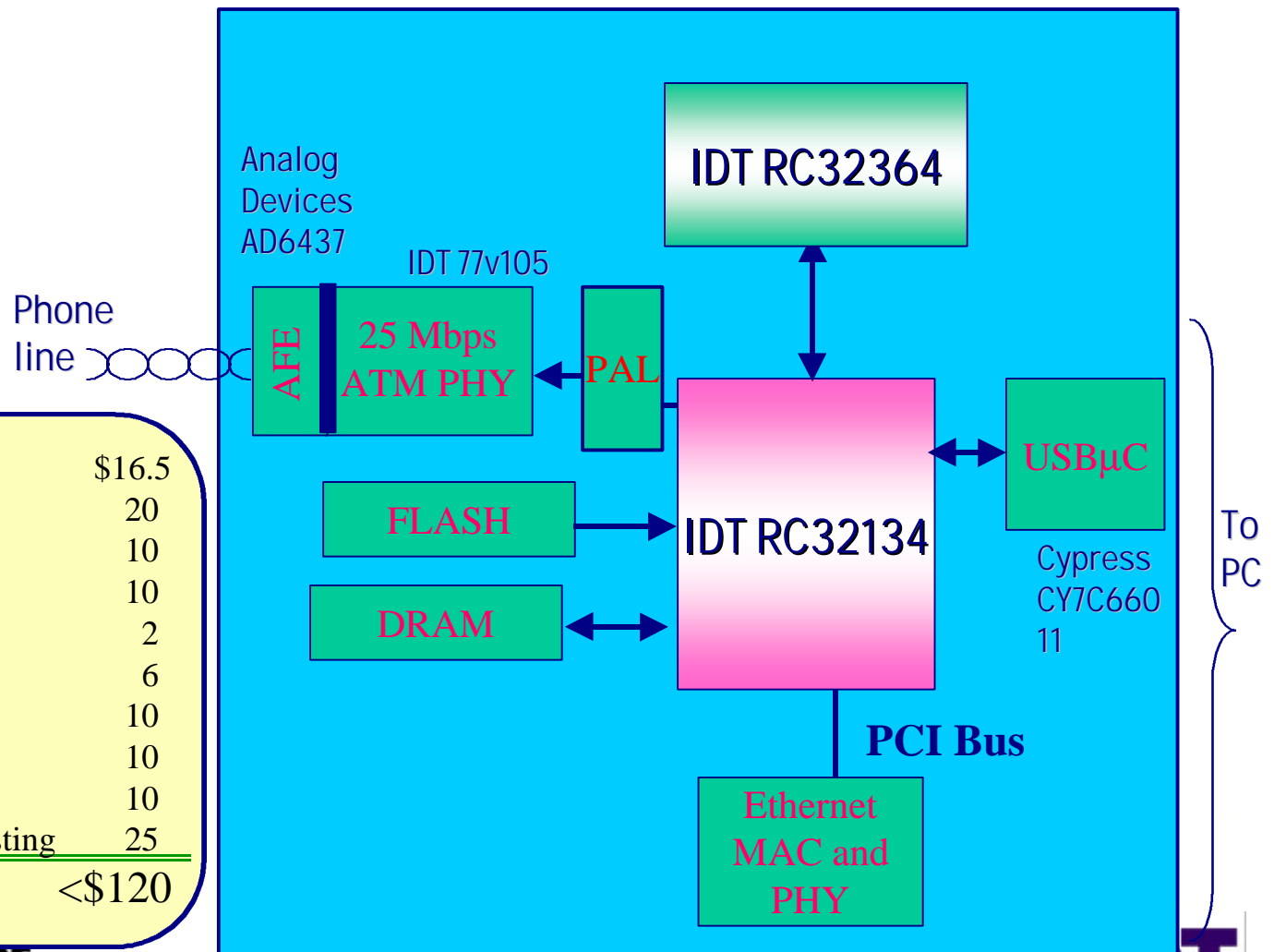


# RC32364 Functions in xDSL modem



- q Dynamic Rate Adaptation control
- q Protocol handling
  - ò F E C, signaling, alarm
- q Loop Management
  - ò Symbol Clock, Carrier Recovery
  - ò BER monitoring, AOC/EOC Processing
- q Network Timing Reference (NTR)
- q Soft SAR for ATM
- q Provides up to 80 mips performance as required

# RC32364 / RC32134 xDSL Modem



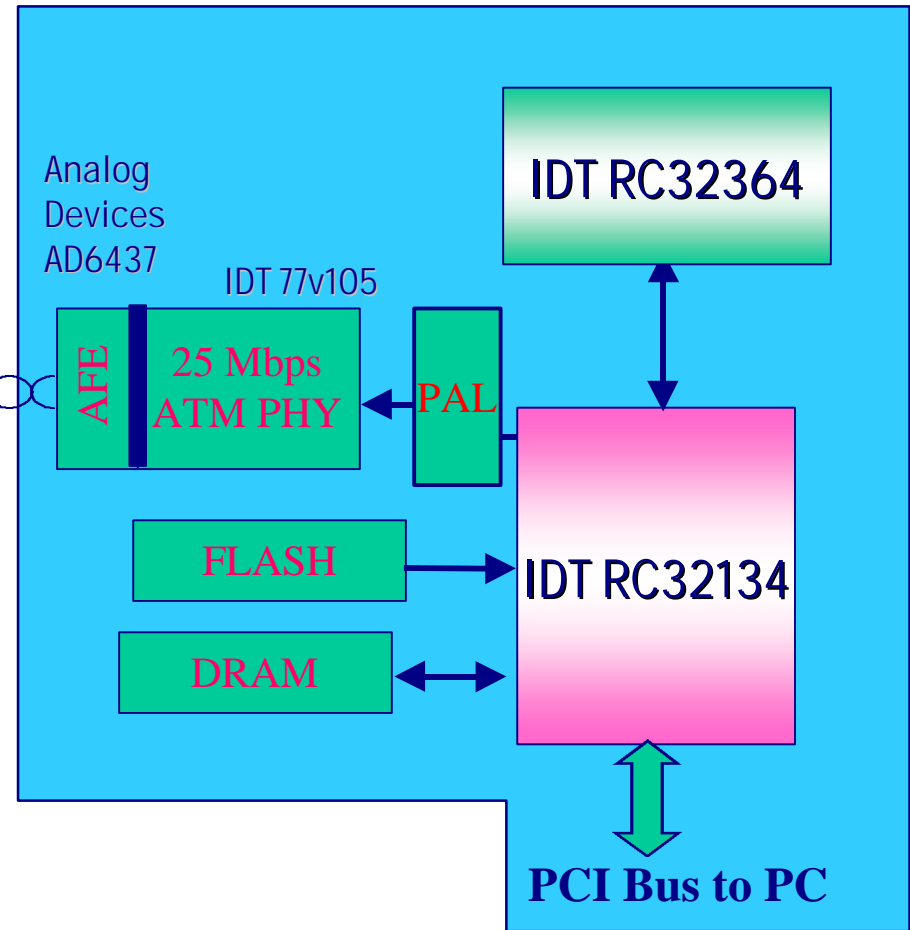
RC32364_133 MHz	\$16.5
RC32134	20
AD6437 AFE	10
IDT77v105 ATM PHY	10
USB device controller	2
Ethernet MAC / PHY	6
PAL	10
4 MB DRAM	10
1 MB FLASH	10
Hardware, manufacturing, testing	25
<b>Total</b>	<b>&lt;\$120</b>

**RISController**

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# RC32364/32134-based xDSL NIC Card



RC32364_133 MHz	\$16.5
RC32134	20
AD6437 AFE	10
IDT77v105 ATM PHY	10
PAL	5
2 MB DRAM	5
Hardware, manufacturing, testing	18
<hr/>	
<b>Total</b>	<b>&lt;\$90</b>

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# RC32364 Functions



- q Soft SAR for ATM
    - ò AAL5 functions (SAR, CRC-32, AAL5 trailer)
    - ò OAM functions (CRC-10, performance monitoring)
    - ò AAL1 and circuit emulation (CES2.0)
    - ò ATM UNI Signaling
    - ò Tunneling and other protocols
  - q Generation of DMT
  - q Adaptive rate control and bit loading
  - q Forward Error Correction (FEC)
  - q Loop management
    - ò Symbol clock, carrier recovery
    - ò Bit Error Rate (BER) monitoring
    - ò Embedded Operation Channel (EOC) and ADSL Operation Channel (AOC) Processing
  - q Network Timing Reference (NTR) Processing
- Estimated required performance ~ 100-120MIPS





# Key Features of RC32364 and RC32134

Feature	Benefits
<u>Complete CPU subsystem solution</u> Low cost On-chip required system functionality SDRAM Control, UART, Timers ...	System solution below \$120 Rapid Time to market Reduced board real estate 300 MB/s DRAM bw sustains line speed On-chip timers to support RTOS On-chip UART for debug and diagnostics
<u>High performance CPU</u> Non Blocking loads Cache locking Prefetch instruction DSP instructions	Migrate more hardware functions to software soft SAR Can scale for RADSL rates up to 8 Mbps Flexibility to easily upgrade the system Lower system cost
<u>Access to PCI</u>	Easy system expansion Used in Plug-in cards
<u>Low power CPU Subsystem</u>	Increased Reliability Lower power budget
<u>Code compatible with RISController processor family</u>	Reuse of code for DSLAM and other apps Reuse of tools



# RC32364 and RC32134 Advantages



- q Complete CPU subsystem solution
  - ò Access to PCI
  - ò Flexible design
- q Best in price-performance
  - ò Can be scaled from DMT.Lite up to 8 Mbs
  - ò Can be scaled to handle up to 6 ADSL channels
  - ò Handles overhead for Rate Adaptive DSL
- q Excellent set of design and development tools