



IDT MIPS Software Developer's Guide, Vol. 1

Notes

Supplemental Information

This Document Errata reflects all changes found in the December 1998 version of the *IDT MIPS Microprocessor Family Software Developer's Guide, Vol. 1*.

Revision History

September 26, 2000: Added Item #1.

Errata Items

Item #1 - Changes to Cache Error (CacheErr) Register

Issue: In Chapter 3, RC5000 has been added to the list of other devices in the heading for section Cache Error (CacheErr) Register. Also, the following sentence has been added to this section: "When a read response (cached or uncached) returns with bad parity, this exception is also taken."