The ColdFire® Family

ColdFire represents a revolutionary new microprocessor architecture that has been optimized for embedded processing applications. It brings new levels of price and performance to cost-sensitive high volume markets. Based on the concept of Variable-Length RISC technology, ColdFire combines the architectural simplicity of conventional 32-bit fixed-length RISC with a memory-saving variable-length instruction set. By employing a variable-length instruction set architecture, ColdFire RISC processors are tuned to offer embedded processor designers significant system-level advantages over conventional fixed-length RISC architectures. Binary code for ColdFire processors is denser and therefore takes up less program memory than conventional 32-bit fixed-length machines. This improved code density results in systems that require less memory for a given application and also allows the use of slower and less costly memory to achieve a given performance level.

The ColdFire Family consists of:

	User's Manual	Presentation	Product Brief	Tools	White Paper
• General ColdFire	*X	X			X
• MCF5102	X		X		
• MCF5202	X		X	X	
• MCF5203			X		
• MCF5204	X		X	X	
• MCF5206	X		X	X	
• MCF5206e	X		X	X	
• MCF5307	X		X	X	
ColdFire Cores					
• Version 2	X				
• Version 3	X				X
• Version 4		X			

* MCF5200 PRM

Main Menu 68000 Family 68300 Family